



Welcome United States Patent and Trademark  
Office

## Search Results

BROWSE

SEARCH

IEEE XPLORE  
GUIDE

Results for "((accelerat\* and (pld or fpga) and (hdl or verilog or vhdl))  
<in>metadata)"

Your search matched 30 of 1340257 documents.

A maximum of 30 results are displayed, 25 to a page, sorted by Relevance in  
Descending order.

### » Search Options

View Session History

New Search

### Modify Search

((accelerat\* and (pld or fpga) and (hdl or verilog or vhdl))<in>metadata)

☐ Check to search only within this results set

Display  
Format:

☒ Citation ☐ Citation & Abstract

[view selected items](#)

[Select All](#) [Deselect All](#)

### » Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

- ☐ **26. A reconfigurable and flexible parallel 3D vision sy  
mobile robot**  
Hou, K.M.; Belloum, A.; Yao, E.; Tu, X.W.; Shawky, M  
Mayorquim, J.L.; Trihandoyo, A.; Jardin, B.;  
Computer Architectures for Machine Perception, 199:  
15-17 Dec. 1993 Page(s):215 - 221  
Digital Object Identifier 10.1109/CAMP.1993.622475  
AbstractPlus | Full Text: [PDF\(500 KB\)](#) IEEE CNF  
Rights and Permissions
- ☐ **27. Implementation of the Silicon Track Card (STC) as  
programmable-chip (SOPC)**  
Lalam, A.; Perry, R.;  
SoutheastCon, 2002. Proceedings IEEE  
5-7 April 2002 Page(s):108 - 112  
Digital Object Identifier 10.1109/2002.995568  
AbstractPlus | Full Text: [PDF\(404 KB\)](#) IEEE CNF  
Rights and Permissions
- ☐ **28. Implementation and analysis of numerical compo  
reconfigurable computing**  
Ligon, W.B., III; Monn, G.; Stanzione, D.; Stivers, F.;  
Aerospace Conference, 1999. Proceedings. 1999 IEE  
Volume 2, 6-13 March 1999 Page(s):325 - 335 vol.2  
Digital Object Identifier 10.1109/AERO.1999.793177  
AbstractPlus | Full Text: [PDF\(860 KB\)](#) IEEE CNF  
Rights and Permissions
- ☐ **29. Quantitative analysis of floating point arithmetic c  
custom computing machines**  
Shirazi, N.; Walters, A.; Athanas, P.;  
FPGAs for Custom Computing Machines, 1995. Proc  
Symposium on

19-21 April 1995 Page(s):155 - 162  
Digital Object Identifier 10.1109/FPGA.1995.477421  
[AbstractPlus](#) | Full Text: [PDF\(516 KB\)](#) IEEE CNF  
[Rights and Permissions](#)

- ☐ 30. **HGA: A Hardware-Based Genetic Algorithm**  
Scott, S.D.; Samal, A.; Seth, S.;  
Field-Programmable Gate Arrays, 1995. FPGA '95. P  
Third International ACM Symposium on  
1995 Page(s):53 - 59  
[AbstractPlus](#) | Full Text: [PDF\(696 KB\)](#) IEEE CNF  
[Rights and Permissions](#)

[Help](#) [Cont](#)  
[Se](#)

© Copyright

Indexed by  
 Inspec

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	0	(hardware adj accelerat\$3) same (high adj level adj language) same (hdl or vhdl or verilog) same (pld or fpga or programmable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 08:33
L6	4	(hardware adj accelerat\$3) same (high adj level adj language)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 08:30
L7	1	(hardware adj accelerat\$3) same ((high adj level adj language) od "C" or "C++") same (hdl or vhdl or verilog) same (pld or fpga or programmable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 08:34
L8	1	(hardware adj accelerat\$3) same ((high adj level adj language) or "C" or "C++") same (hdl or vhdl or verilog) same (pld or fpga or programmable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 09:34
L10	34	(hardware adj accelerat\$3) same ((high adj level adj language) or "C" or "C++") same (read\$4 or writ\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 09:35
L11	6	(hardware adj accelerat\$3) same ((high adj level adj language) or "C" or "C++") same (read\$4 or writ\$4) same (pld or pla or fpga or programmable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 09:58
L12	28	(hardware adj accelerat\$3) same ((high adj level adj language) or "C" or "C++" or (source adj code)) with (read\$4 or writ\$4 or pint\$3 or select\$3 or segment\$3 or seccion)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 09:59
L13	31	(hardware adj accelerat\$3) same ((high adj level adj language) or "C" or "C++" or (source adj code)) with (read\$4 or writ\$4 or point\$3 or select\$3 or segment\$3 or seccion)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 10:03
L14	2	(hardware adj accelerat\$3) same ((high adj level adj language) or "C" or "C++" or (source adj code)) with (read\$4 or writ\$4 or point\$3 or select\$3) with (portion or part or partition\$3 or segment or section or piece)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 10:08

## EAST Search History

L15	0	(hardware adj accelerat\$3) same ((high adj level adj language) or "C" or "C++" or (source adj code)) with (point\$3 or select\$3) with (portion or part or partition\$3 or segment or section or piece)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 10:09
L16	36	(hardware adj accelerat\$3) and ((high adj level adj language) or "C" or "C++" or (source adj code)) with (point\$3 or select\$3) with (portion or part or partition\$3 or segment or section or piece)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 14:05
L17	7	(hardware adj accelerat\$3) with read\$3 with writ\$3 with port	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 14:07
L19	0	((hardware adj accelerat\$3) with read\$3 with writ\$3 with port) same (pld or pla or fpga or programmable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 14:06
L20	7	((hardware adj accelerat\$3) or hwa) with read\$3 with writ\$3 with port	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 14:07
S1	0	(hardware near3 accelerat\$3) same hdl same (programmable adj device) same memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:26
S2	2	(hardware near3 accelerat\$3) same hdl same (programmable adj device)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:28
S3	0	(hardware near3 accelerat\$3) same (programmable adj device) same processor and hdl	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:28
S4	2	(hardware near3 accelerat\$3) same (programmable adj device) and hdl	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:29

## EAST Search History

S5	36	(hardware near3 accelerat\$3) and hdl and memory and (programmable adj device)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:31
S6	17	(hardware near3 accelerat\$3) same hdl and memory and (programmable _ adj device)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:34
S7	18	(hardware near3 accelerat\$3) same (hdl or vhdl or verilog) and memory and (programmable adj device)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:45
S8	63	(hardware near3 accelerat\$3) same (hdl or vhdl or verilog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:46
S9	39	(hardware near3 accelerat\$3) same (hdl or vhdl or verilog) and ((programmable adj device) or pld or fpga)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:51
S10	3	(hardware near3 accelerat\$3) same ((portion or part or segment or section or piece) near3 (hdl or vhdl or verilog)) and ((programmable adj device) or pld or fpga)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 16:26
S11	3	(hardware near3 accelerat\$3) same ((portion or part or segment or section or piece) near3 (hdl or vhdl or verilog))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 16:26
S12	23	(hardware near3 accelerat\$3) same (portion or part or segment or section or piece) same (hdl or vhdl or verilog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 16:36
S13	2	"20020133325"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 16:50

## EAST Search History

S14	0	(programmable device) same processor same (hardware adj accelerat\$3) and ((hdl or vhdl or verilog) same (part or partition\$3 or portion or segment or section or piece) same (hardware adj accelerat\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 16:53
S15	4	(programmable device) same processor same (hardware adj accelerat\$3) and ((hdl or vhdl or verilog) same (part or partition\$3 or portion or segment or section or piece) same (hardware adj accelerat\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 16:55
S16	0	(programmable adj device) same processor same (hardware adj accelerat\$3) and ((hdl or vhdl or verilog) same (part or partition\$3 or portion or segment or section or piece) same (hardware adj accelerat\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 16:53
S17	1	(programmable near3 device) same processor same (hardware adj accelerat\$3) and ((hdl or vhdl or verilog) same (part or partition\$3 or portion or segment or section or piece) same (hardware adj accelerat\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 16:55
S18	27	programmable near3 (hardware adj accelerator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 17:14
S19	6	programmable near3 (hardware adj accelerator) and (hdl or vhdl or verilog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 17:15
S20	6	programmable near3 (hardware adj accelerator) and (hdl or vhdl or verilog) and processor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 17:23
S21	0	(hardware near3 accelerat\$3 near3 logic) same programmable and ((hdl or vhdl or verilog) near3 (part or partition\$3 or portion or segment or section or piece))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 17:24

## EAST Search History

S22	3	(hardware near3 accelerat\$3 near3 logic) same programmable and (hdl or vhdl or verilog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 17:34
S23	0	(generat\$3 near3 hardware near3 accelerat\$3 near3 logic near3 programmable) and (hdl or vhdl or verilog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 17:36
S24	1	(hardware near3 accelerat\$3 near3 logic near3 programmable) and (hdl or vhdl or verilog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:13
S25	4	("6912706" "6877150").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 13:43
S26	0	(hardware near3 accelerat\$3) near3 ((hdl or vhdl or verilog) near3 (part or portion or partition\$3 or segment or section or piece))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:38
S27	2	(hardware near3 accelerat\$3) near3 ((hdl or vhdl or verilog) same (part or portion or partition\$3 or segment or section or piece))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:38
S28	3	(hardware near3 accelerat\$3) same ((hdl or vhdl or verilog) near3 (part or portion or partition\$3 or segment or section or piece))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 12:13
S29	5	hardware adj accelerat\$3 adj logic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 12:19
S30	32	(hardware adj accelerat\$3) same logic same (programmable or pld or fpga) and (hdl or vhdl or verilog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 12:22
S31	2	(hardware adj accelerat\$3) same logic same (programmable or pld or fpga) same (hdl or vhdl or verilog)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 12:29

## EAST Search History

S32	2	(hardware adj accelerat\$3) same ((hdl or vhdl or verilog) near3 (portion or part or partition\$3 or segment or section or piece))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 12:37
S33	22	(hardware adj accelerat\$3) same (hdl or vhdl or verilog) same (portion or part or partition\$3 or segment or section or piece)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 13:08
S36	0	\$51633-\$.did and gupta.in.	EPO; DERWENT	OR	ON	2006/04/13 13:13
S37	2940	gupta.in.	DERWENT	OR	ON	2006/04/13 13:17
S38	325	gupta.in. and (Compilation of remote procedure calls between a timed HDL model on a reconfigurable hardware platform and an untimed model on a sequential computing platform).ti.	DERWENT	OR	ON	2006/04/13 13:19
S39	1	gupta.in. and HDL.ti.	DERWENT	OR	ON	2006/04/13 13:20
S40	0	gupta.in. and HDL.ti. and accelerat\$3	DERWENT	OR	ON	2006/04/13 13:21
S41	19	gupta.in. and accelerat\$3	DERWENT	OR	ON	2006/04/13 13:28
S42	0	gupta.in. and accelerat\$3 and hdl	DERWENT	OR	ON	2006/04/13 13:21
S43	0	gupta-Sanjay.in.	DERWENT	OR	ON	2006/04/13 13:29
S44	0	gupta-sanjay.in.	DERWENT	OR	ON	2006/04/13 13:29
S45	12	gupta-sanjay.in.	EPO; DERWENT	OR	ON	2006/04/13 13:29
S46	72	(hardware adj accelerat\$3) near3 (programmable or pld or fpga)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 14:05
S47	1	((programmable adj device) or pld or fpga) near3 (hardware adj accelerat\$3) near3 processor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 14:06